

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with G. Gregory Schivley on October 9, 2008.

2. The application has been amended as follows:

- In line 5 of claim 1, please insert the word – voltage – after the phrase “gate-applied”. Therefore, the phrase should read – a circuit for changing a gate-applied voltage –.
- In line 3 of claim 8, please insert the word – voltage – after the phrase “source-applied”. Therefore, the phrase should read – a circuit for changing a source-applied voltage –.

EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

3. The following is an examiner's statement of reasons for allowance:

Regarding claim 1, the primary reason for allowance is that the prior art fails to teach or reasonably suggest a circuit for changing a gate-applied that changes a voltage applied to each gate of a plurality of the transistors; the circuit for changing a the gate-applied applying a first voltage to each gate of a plurality of the transistors in a first voltage source while in an accumulation state when carriers are generated from the photo diode in response to received light, the circuit for changing the gate-applied voltage applying a second voltage to each of the gates of the plurality of transistors in the plurality of lines while in a reset state after the accumulation state, and the circuit for changing the gate-applied voltage applying a third voltage from a second voltage source to each of the gates of the plurality of transistors in a selected one of the plurality of lines while in a reading out state when a signal in response to carriers accumulated in the accumulation state is read out after the reset state.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JOHN M. VILLECCO whose telephone number is (571)272-7319. The examiner can normally be reached on Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sinh Tran can be reached on (571) 272-7564. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/JOHN M. VILLECCO/
Primary Examiner, Art Unit 2622
October 9, 2008